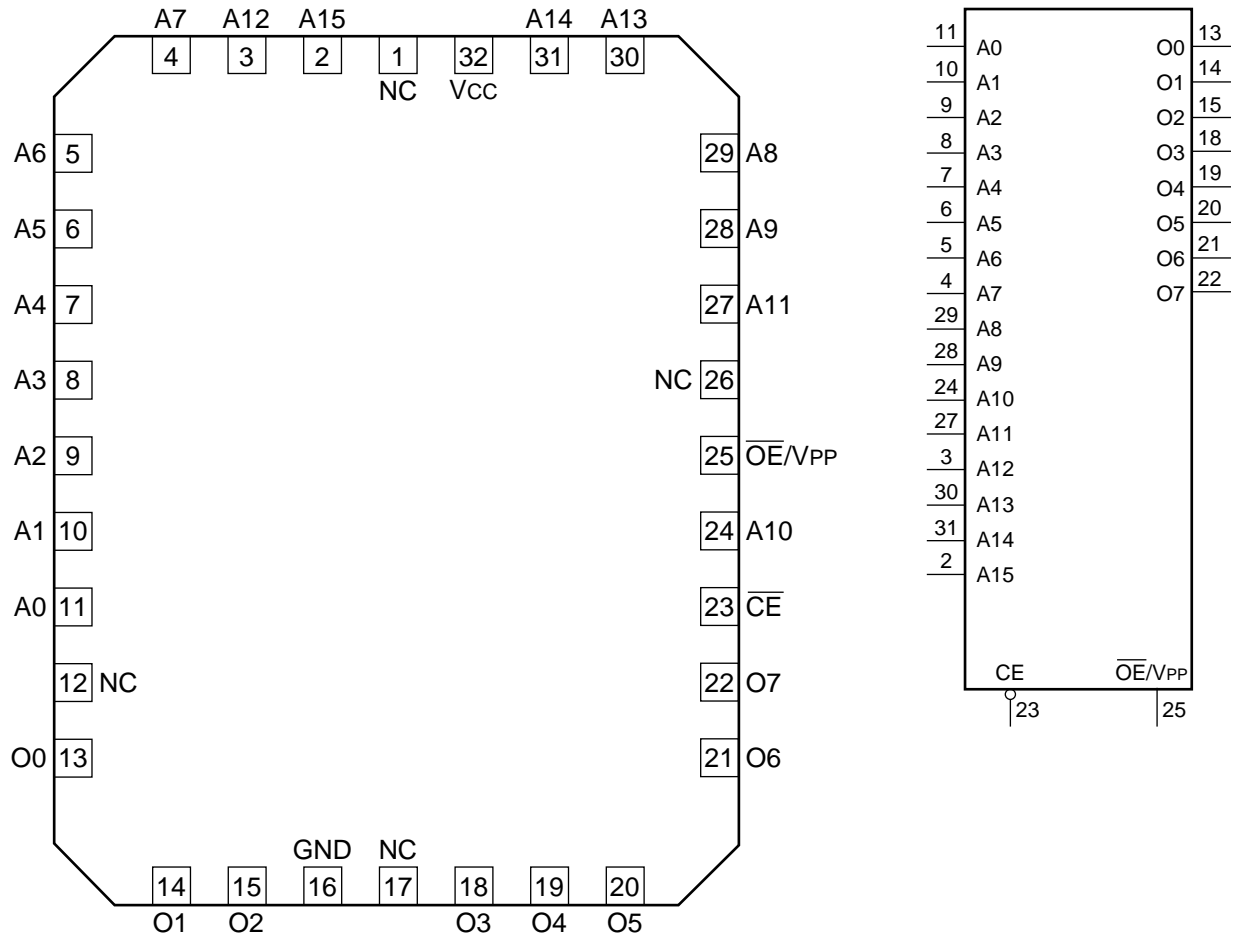


C-MOS 512K (65536 × 8)-BIT UV-EPROM

—BOTTOM VIEW—



**INPUT**

- A0 - A15 : ADDRESS BUS
- $\overline{CE}$  : CHIP ENABLE
- $\overline{OE/VPP}$  : OUTPUT ENABLE

**OUTPUT**

- O0 -O7 : DATA BUS

